

## Four-phase Quadrature Oscillator using DO-CCCFTA

**Abstract.** This article introduces novel current-mode quadrature oscillator circuit based on DO-CCCFTA and three grounded passive components. The oscillator circuit can be generated by four-phase sinusoidal current signal. The oscillation condition can be non-interactively controlled from oscillation frequency. Furthermore, its high impedance of outputs can directly connect in current-mode circuit and use only grounded passive components that are very suitable to be furthered fabricated into integrated circuit. The PSPICE simulations and experimental method were implemented for theoretically analysing the proposed oscillator.

**Streszczenie.** Przedstawiono nową koncepcję układu generatora bazującego na układzie DO-CCCFTA oraz trzech pasywnych elementach. Generator może wytwarzać cztery prądy o sterowanej częstotliwości. Układy generatorów mają wyjścia prądowe. Teoretyczna analiza potwierdzona jest symulacją i eksperymentem. (Cztery kwadraturowe generatory bazujące na układzie DO-CCCFTA)

**Keywords:** four-phase quadrature oscillator, current-mode, non-interactive control, DO-CCCFTA.

**Słowa kluczowe:** generator o wyjściu prądowym, układy DO-CCCFTA.

### Introduction

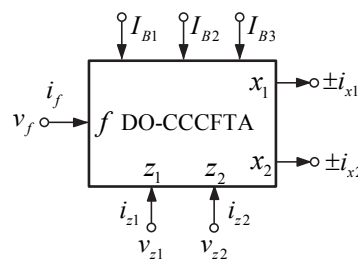
The quadrature oscillator (QO) is an oscillator circuit that provides two sinusoidal signals with  $90^\circ$  phased difference. A quadrature signal was occasionally implemented for quadrature mixers and the modulator with single-sideband modulators in electronic telecommunications [1]. In the last decade, there were papers concerning the designs of oscillator circuits with can provide quadrature signals having been suggested as current-mode technique stating that the circuit designed from current-mode technique were advantageous for designing; larger dynamic range, spontaneously wide bandwidth, higher slew-rate, higher linearity and lower rate of power consumption [2-3].

Recently, the researches in [4-27] conducted the study regarding the use of a brand-new active element to design an oscillator circuit with current-mode technique. From literature survey, the quadrature oscillators were widely implemented using different active elements [11-27]. However, there are still some weaknesses found as follows:

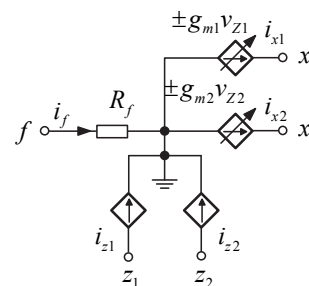
1. Non-interactively controlled for both of the condition and frequency of oscillation [11-14] is inconvenient for implementation.
2. The condition and frequency of oscillation cannot be electronically adjusted [15-16], which are unsuitable to be used in control systems and microcontroller system.
3. The floating passive elements [12-18] were not desirable for the fabrication of integrated circuit.
4. The excessive use of the passive resistors [19-27], consequently, the power consumption became increased.

This paper highlights on a current-mode four-phase quadrature oscillator circuit that needs only single active element namely dual-output current controlled current follower transconductance amplifier (DO-CCCFTA). This

circuit generates the four-phase current output signals with a 90-degree phase deference. Additionally, it is suitable to use grounded capacitors and resistor for the four-phase oscillator circuit to be developed into an integrated circuit [5-6]. Moreover, it is possible to instigate electronic method and non-interactive dual current control to adjust the condition and the frequency of oscillation.



(a) Symbol of DO-CCCFTA



(b) Equivalent circuit of DO-CCCFTA

Fig. 1. Symbol and equivalent circuit of DO-CCCFTA.

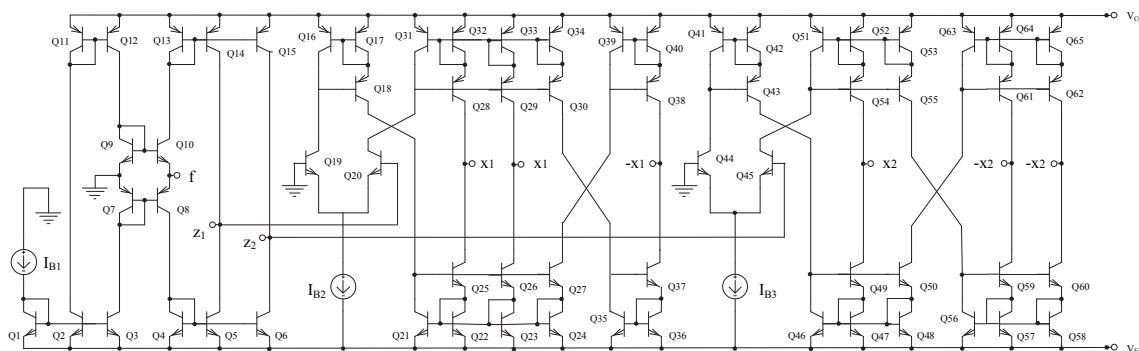


Fig. 2. Internal construction of DO-CCCFTA

### Basic concept of DO-CCCFTA

The DO-CCCFTA is a modification form of current controlled current follower transconductance amplifier (CCCFTA). The characteristics of DO-CCCFTA were represented by the following hybrid matrix:

$$(1) \quad \begin{bmatrix} v_f \\ i_z, i_{zc} \\ i_{x1} \\ i_{x2} \end{bmatrix} = \begin{bmatrix} R_f & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 \\ 0 & 0 & \pm g_{m1} & 0 \\ 0 & 0 & 0 & \pm g_{m2} \end{bmatrix} \begin{bmatrix} i_f \\ v_x \\ v_z \\ v_{zc1} \end{bmatrix}$$

$R_f$  is the parasitic resistance at  $f$  port, and it is capable of being adjusted by  $I_{B1}$ . The  $g_{m1}$  and  $g_{m2}$  are the transconductance to be adjusted by  $I_{B2}$  and  $I_{B3}$ , respectively. For a bipolar junction transistor, the  $R_f$ ,  $g_{m1}$  and  $g_{m2}$  of DO-CCCFTA can be specified as the following equations :

$$(2) \quad R_f = \frac{V_T}{2I_{B1}}$$

$$(3) \quad g_{m1} = \frac{I_{B2}}{2V_T}$$

$$(4) \quad g_{m2} = \frac{I_{B3}}{2V_T}$$

From Equations (2) – (4),  $I_{B1}$ ,  $I_{B2}$  and  $I_{B3}$  are bias currents of DO-CCCFTA. The symbol and the equivalent circuit of DO-CCCFTA were illustrated in Figs 1 (a) and (b), respectively. The BJT internal construction of DO-CCCFTA was showed in Fig. 2.

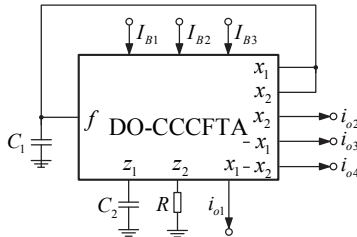


Fig. 3. Four-phase current-mode quadrature oscillator

### Four-phase Current-mode Quadrature Oscillator

The proposed four-phase current-mode quadrature oscillator illustrated in Fig. 3 was consisted of single DO-CCCFTA, two grounded capacitors and single grounded resistor. The characteristic equation of oscillator can be illustrated in Equation (5).

$$(5) \quad s^2 + s \frac{C_2 - g_{m2}RC_1}{R_f C_1 C_2} + \frac{g_{m1}}{R_f C_1 C_2} = 0$$

From Equation (5), the oscillation condition (OC) and oscillation frequency (OF) are as follows:

$$(6) \quad OC: 1 = g_{m2}R$$

$$(7) \quad OF: f_{osc} = \frac{1}{2\pi} \sqrt{\frac{g_{m1}}{R_f C_1 C_2}}$$

By substituting the  $g_{m1}$ ,  $g_{m2}$  and  $R_f$ , the parameters OC and OF are developed into :

$$(8) \quad OC: 2V_T = I_{B3}R$$

$$(9) \quad OF: f_{osc} = \frac{1}{2\pi V_T} \sqrt{\frac{I_{B1} I_{B2}}{C_1 C_2}}$$

From Equations (8) and (9), the OC can be adjusted independently from the OF. The relation of the output signals was defined as:

$$(10) \quad \frac{i_{o2}}{i_{o1}} = \frac{g_{m2}RC_2s}{g_{m1}}$$

$$(11) \quad \frac{i_{o3}}{i_{o2}} = -\frac{g_{m2}RC_2s}{g_{m1}}$$

$$(12) \quad \frac{i_{o4}}{i_{o3}} = \frac{g_{m2}RC_2s}{g_{m1}}$$

$$(13) \quad \frac{i_{o1}}{i_{o4}} = -\frac{g_{m1}}{g_{m2}RC_2s}$$

According to Equation (10) – (13), the oscillator can generate four-phase quadrature sinusoidal output signals.

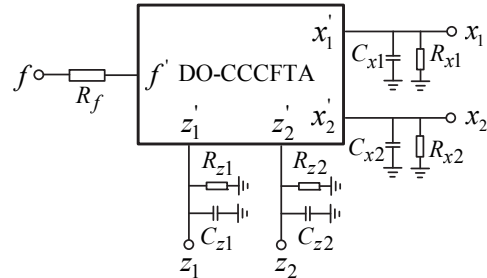


Fig. 4. Parasitic component of DO-CCCFTA

### Analysis of non-ideal case

For non-ideal case, the characteristic equation of DO-CCCFTA in Equation (1) becomes:

$$(14) \quad \begin{bmatrix} v_f \\ i_z, i_{zc} \\ i_{x1} \\ i_{x2} \end{bmatrix} = \begin{bmatrix} R_f & 0 & 0 & 0 \\ \alpha & 0 & 0 & 0 \\ 0 & 0 & \pm \beta_1 g_{m1} & 0 \\ 0 & 0 & 0 & \pm \beta_2 g_{m2} \end{bmatrix} \begin{bmatrix} i_f \\ v_x \\ v_z \\ v_{zc1} \end{bmatrix}$$

The parameters  $\beta$  and  $\alpha$  are the voltage and current transfer, respectively. These parameters were changed on the intrinsic impedances and temperatures. In addition, DO-CCCFTA consists of parasitic elements  $R_f$ ,  $R_z$ ,  $C_z$ ,  $R_x$  and  $C_x$ , as showed in Fig. 4. Then, when  $R_x, R_z \gg R_f$ , the Equations (5) – (7) become

$$(15) \quad \left\{ \begin{aligned} & (C_1 + C_{x1} + C_{x2})(C_2 + C_{z1})C_{z2}R_f s^3 + (C_2 + C_{z1})C_{z2}s^2 \\ & + \frac{1}{R}(C_2 + C_{z1})s^2 - \alpha\beta_2 g_{m2}(C_2 + C_{z1})s + \alpha\beta_1 g_{m1}C_{z2}s \\ & + \frac{1}{R}\alpha\beta_1 g_{m1} \end{aligned} \right\} = 0$$

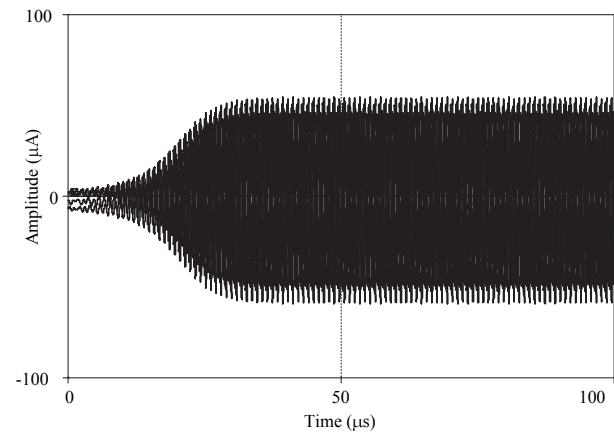
$$(16) \quad OC: (C_1 + C_{x1} + C_{x2})C_{z2}R_f = \frac{1}{R^2} \alpha^3 \beta_1^2 \beta_2 g_{m1}^2 g_{m2}$$

$$(17) \quad OF: f_{osc} = \frac{1}{2\pi V_T} \sqrt{\frac{\alpha\beta_1 g_{m1}}{(C_1 + C_{x1} + C_{x2})(C_2 + C_{z1})C_{z2}R_f R}}$$

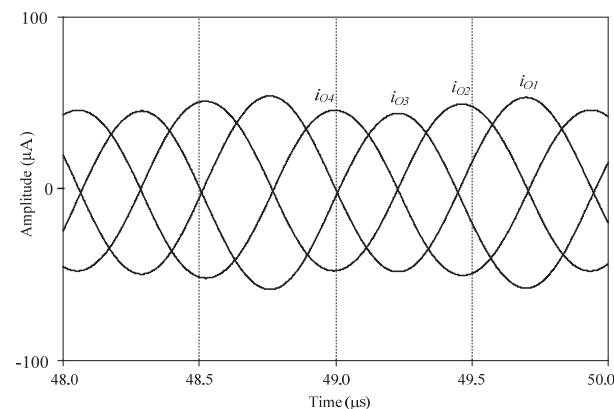
### Simulation Results

To examine the stability of the proposed circuit in Fig. 3, the PSPICE simulation program was used for the examination. The parameters of transistor in [6] were used

for the simulation and the implementation of internal construction of DO-CCCFTA as showed in Fig.2. The simulation was set with  $\pm 1.5V$ ,  $I_{B1} = 100\mu A$ ,  $I_{B2} = 400\mu A$ ,  $I_{B3} = 115\mu A$ ,  $R = 500\Omega$  and  $C_1 = 1nF$ ,  $C_2 = 1nF$ . Fig. 5 demonstrates the simulated quadrature waveforms of output in initial and steady state, the outputs frequency, about 1.05MHz was deviated about 13.92%. The deviation of the OF must be occurred with the parasitic elements and the voltage and current transfer of DO-CCCFTA.



(a) Initial state signals



(b) Steady state signals

Fig. 5. The simulation of output waveforms

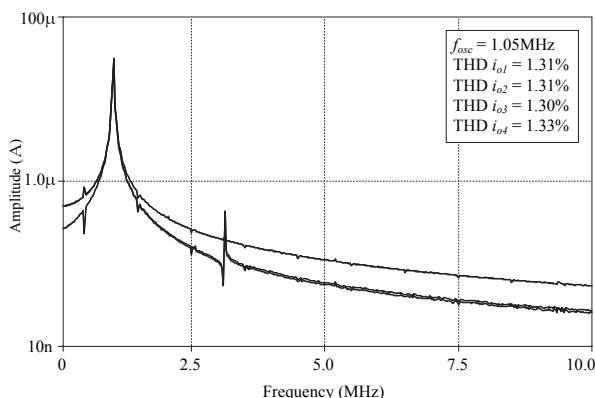


Fig. 6. The spectrum of quadrature oscillator

Fig.6 indicates the result of output spectrum. These results were confirmed for electronic tuning of the OF without effect of the OC. The total harmonic distortion (THD) of  $i_{o1}$ ,  $i_{o2}$ ,  $i_{o3}$  and  $i_{o4}$  were about 1.31%, 1.31%,

1.30% and 1.33%, respectively. Additionally, Fig. 7 illustrates the OF when the bias current  $I_{B2}$  and the value of capacitor are deviated. Moreover, the tolerances of external passive elements were affected to the OF. The Monte Carlo analysis can be computed of the OF by using 100 trials and 10% of the tolerances of the external passive elements. The simulation in Fig.8 (a) and (b) illustrated the spectrums and histograms of the OF, respectively. The maximum, minimum and medium of the OF are 2.158MHz, 0.4941MHz and 1.068MHz, respectively.

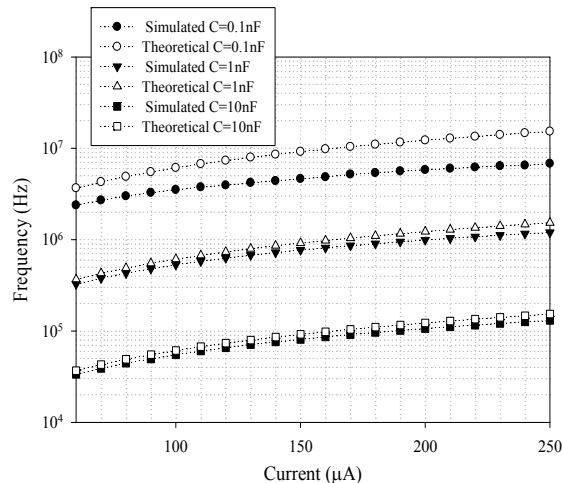
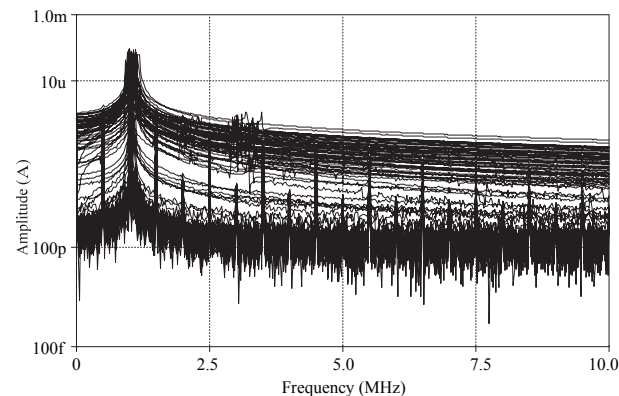
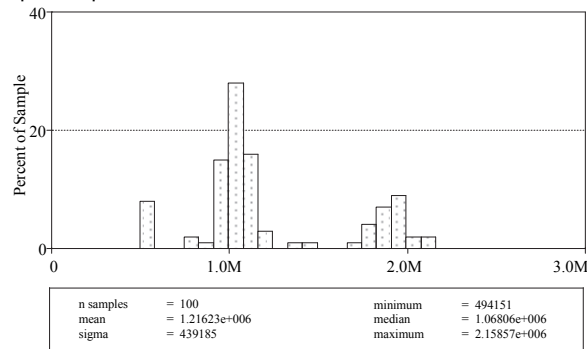


Fig. 7. Electronic tuning of proposed oscillator



(a) Spread spectrums



(b) Histograms

Fig. 8. Monte Carlo simulation

### Experimental Results

The four phase quadrature oscillator in Fig. 3 was experimentally set up by using current feedback operational amplifier AD844 IC and operational transconductance amplifier LM13700 ICs (LM13700 with  $g_m$  about  $I_B / 2V_T$ ) in order to confirm the theoretical prediction. The

implementation of the DO-CCCFTA used in experiment was depicted in Fig. 9. The circuit was biased with  $\pm 5V$  supply voltages and being used, consisting of  $C_1 = C_2 = 1nF$ ,  $I_{B2} = 400\mu A$  and  $I_{B3} = 130\mu A$ . The parasitic resistance  $R_f$  was configured by external passive resistor which was  $R_f = 130\Omega$ . The output currents  $i_{o1}$ ,  $i_{o2}$ ,  $i_{o3}$  and  $i_{o4}$  were converted to output voltage  $v_{O1}$ ,  $v_{O2}$ ,  $v_{O3}$  and  $v_{O4}$ , respectively, by using external passive resistors that were about  $1k\Omega$ . The output voltage waveforms used the oscilloscope Rigol model DS1074B for the measurement.

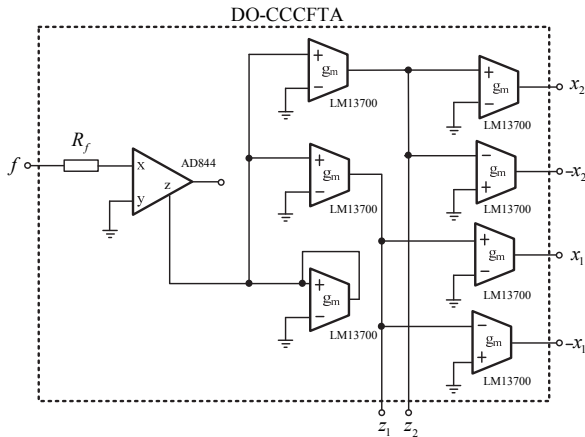
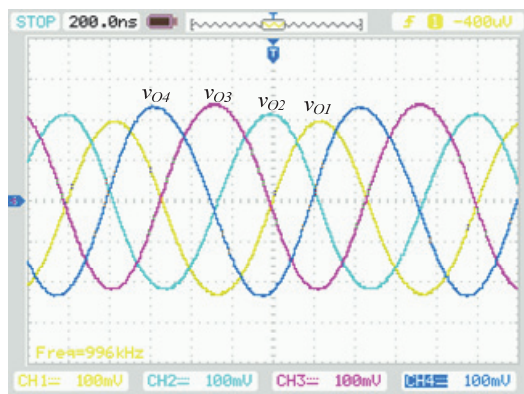
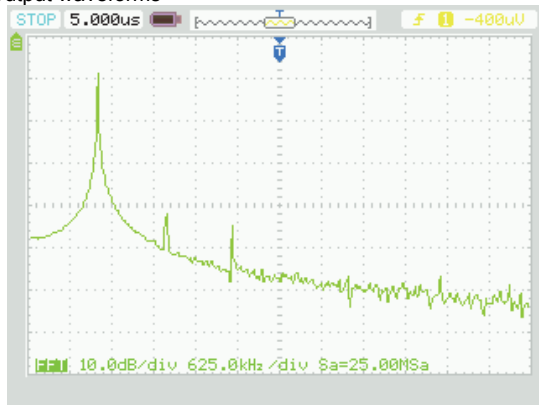


Fig. 9. Internal construction of DO-CCCFTA using commercial ICs.



(a) Output waveforms

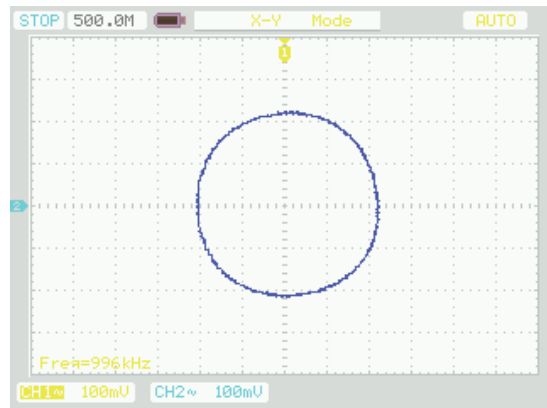


(b) Output spectrum

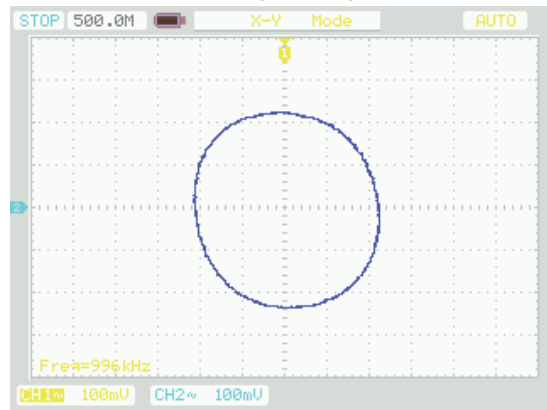
Fig. 10. The output waveforms

Fig 10 (a) displayed the quadrature output waveforms during steady state and Fig. 10 (b) displayed the output spectrum. The oscillation frequency was about 996kHz

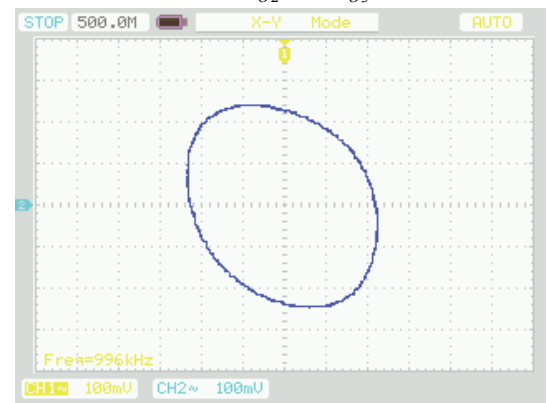
where the calculated value of this parameter from equation (7) yielded 1.22MHz (deviated about 18.36%). As well, the voltage waveforms correlation can be detailed in Figs 11. However, the deviation of frequency and phase were arisen by the tolerances of external passive elements, parasitic elements and transfer errors of DO-CCCFTA as explained in Topic of non-ideal case.



(a) Waveforms correlation of  $v_{O1}$  and  $v_{O2}$



(b) Waveforms correlation of  $v_{O2}$  and  $v_{O3}$



(c) Waveforms correlation of  $v_{O3}$  and  $v_{O4}$

Fig. 11. The waveforms correlation

### Conclusion

This research study proposes the current-mode four-phase sinusoidal quadrature oscillator. The current-mode oscillator has a single active element. It constructs single DO-CCCFTA and three grounded passive components. Consequently, the oscillation frequency can be electronically tuned with non-interactive dual-current control from the oscillation condition which is a benefit of oscillator circuit. With high output impedance, the oscillator is suitable

for connecting in current-mode circuit. The simulation of Monte Carlo method can demonstrate the output frequency of proposed four-phase current-mode quadrature oscillator. Moreover, PSPICE simulation and experimental was applied to certify the theoretical method.

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#### REFERENCES

- [1] Khan I.A. and Khawaja S., An Integrable gm-C Quadrature Oscillator, *International Journal of Electronics*, 87 (2000), No. 11, 1353-1357
- [2] Toumazou C., Lidgey F.J., D.G. Haigh, *Analogue IC Design : the Current-mode Approach*, London : Peter Peregrinus, (1990)
- [3] Cam U., Toker A., Cicekoglu O., Kuntmam H., Current-mode High Output-impedance Sinusoidal Oscillator Configuration Employing Single FTFN, *Analog Integrated Circuits and Signal Processing*, 24 (2000), No. 3, 231-238
- [4] Jaikla W., Lahiri A., Resistor-less Current-mode Four-phase Quadrature Oscillator using CCCDTAs and Grounded Capacitors, *International Journal of Electronics and Communications (AEU)*, 66 (2012), No. 3, 214-218.
- [5] Tanaphatsiri C., Jaikla W., Electronically Tunable Four Phase Quadrature Oscillator Employing Current-controlled Current Conveyor Transconductance Amplifiers, 2011 Sixth IEEE International Symposium on Electronic Design, Test and Application, (2011), 89-92
- [6] Summart S., Thongsopa C., Jaikla W., OTA Based Current-mode Sinusoidal Quadrature Oscillator with Non-interactive Control, *Przeglad Elektrotechniczny (Electrical Review)*, 88 (2012), No. 7, 14-17
- [7] Summart S., Thongsopa C., Jaikla W., CCCIs-based Sinusoidal Quadrature Oscillators with Non-interactive Control of Condition and Frequency, *Indian Journal of Pure & Applied Physics*, 52 (2014), No. 4, 277-283
- [8] Li Y.A., Electronically Tunable Current-mode Biquadratic Filter and Four-phase Quadrature Oscillator, *Microelectronics Journal*, 45 (2014), No. 3, 330-335
- [9] Jantakun A., Jaikla W., Current-mode Quadrature Oscillator based on CCCDTAs with Noninteractive Dual-current Control for Both Condition of Oscillation and Frequency of Oscillation, *Turkish Journal of Electrical Engineering & Computer Sciences*, 21 (2013), No.1, 81-89
- [10] Keskin A.U., Bielek D., Current Mode Quadrature Oscillator using Current Differencing Transconductance Amplifiers (CDTA), *IEE Proceedings - Circuits, Devices and Systems*, 153 (2006), No. 3, 214-218
- [11] Keawon R., Jaikla W., A Resistor-less Current-mode Quadrature Sinusoidal Oscillator Employing Single CCCDTA and Grounded Capacitors, *Przeglad Elektrotechniczny (Electrical Review)*, 87 (2017), Nr. 8, 138-141
- [12] Pittala C.S.; Srinivasulu A., Quadrature Oscillator Using Operational Transresistance Amplifiers, 2014 International Conference on Applied Electronics, (2014), 117-120
- [13] Jin J., Wang C., Single CDTA-based current-mode quadrature oscillator, *International Journal of Electronics and Communications (AEU)*, 66 (2012), 933-936
- [14] Abaci A., Yuce E., Modified DVCC based quadrature oscillator and lossless grounded inductor simulator using grounded capacitor(s), 76 (2017), 86–96
- [15] Torteanchai U., Phatsomsiri P., Kumngern M., Quadrature Oscillator Using Operational Transresistance Amplifiers, 7th International Conference on Intelligent Systems, Modelling and Simulation (ISMS), (2016), 403-406
- [16] Tandell S., Bhagat N., hot U.P., CMOS CFOA based Quadrature Oscillator, 2020 International Conference on Electronics and Sustainable Communication Systems (ICESC), 66 (2020), 940-943
- [17] Pushkar L.K., Kumar G.R., Gupta K., Vivek P., Ashraf J., New VD-DIBA-Based Single-Resistance-Controlled Sinusoidal Oscillator, *Circuits and Systems*, (2016), 7, 4145-4153
- [18] Uttaphut P., New current-mode quadrature sinusoidal oscillator using single DVCCCTA as active element, *Przeglad Elektrotechniczny (Electrical Review)*, 92 (2016), No. 9, 229-232
- [19] Kumbun J., Silapan P., Siripruchyanun M., Prommee P., A Current-mode Quadrature Oscillator Based on CC-CFAs, 2009 6th International Conference on Electrical Engineering/Electronics, Computer, Telecommunications and Information Technology, 1 (2009), 542-545
- [20] Tangsrirat W., Pukkalanun T., Surakamponorn W., CDBA-based Universal Biquad Filter and Quadrature Oscillator, *Active and Passive Electronic Components*, 2008 (2008), 1-6
- [21] Jaikla W., Noppakarn A., Lawanwisu S., New Gain Controllable Resistor-less Current-mode First Order Allpass Filter and Its Application, *Radioengineering*, 21 (2012), No. 1, 594-599
- [22] Jantakun A., Current-mode Quadrature Oscillator using CCCCTAs with Non-interactive Current Control for CO, FO and Amplitude, *Journal of Microelectronics, Electronic Components and Materials*, 45 (2015), No. 1, 47-56
- [23] Pitaksuttayaprot K., Jaikla W., Implementation of second order current mode quadrature sinusoidal oscillator with current controllability, *International Journal of Electrical, Computer, Energetic, Electronic and Communication Engineering*, 7 (2013), No. 3, 304-306
- [24] Chaichana A. and Jaikla W., Electronically tunable current-mode quadrature oscillator derived from first-order allpass filter, (2014) *European Modelling Symposium (EMS)*, (2014), 458-461
- [25] Prasad D., M Srivastava., D. Ram, Electronically controllable fully-uncoupled explicit current-mode quadrature oscillator using VDTAs and grounded capacitors, *Circuits and Systems*, 4 (2013), 169-172
- [26] Kamath D.V., OTA based current-mode sinusoidal quadrature oscillator circuits, *International Journal of System Modeling and Simulation*, 1 (2016), No.1, 1-6
- [27] Srivastava M., Prasad D., VDCC based dual-mode quadrature sinusoidal oscillator with outputs at appropriate impedance levels, *Theoretical and Applied Electrical Engineering*, 14 (2016), No. 2, 168-177