Five-phase cascade inverter controlled by signals based on the Haar wavelet transform

Streszczenie. W artykule opisano syntezę wartości chwilowej napięcia wyjściowego wielopoziomowego pięciofazowego falownika kaskadowego. Opisano analityczną metodę wyznaczania zbioru falek ortogonalnych Haara oraz propozycję syntezy przebiegów wyjściowych falownika w oparciu o transformatę falkową. Na podstawie falki Haara obliczono sygnały sterujące kluczami elektronicznymi połączonych kaskadowo falowników tworzących wielopoziomowy pięciofazowy falownik napięcia. Przeprowadzono symulację współpracy takiego falownika z obciążeniem rezystancyjno-indukcyjnym. (Pięciofazowy falownik kaskadowy sterowany wektorami obliczonymi na podstawie falki Haara).

Abstract. The article describes the synthesis of the instantaneous value of the output voltage of a multi-level five-phase cascade inverter. An analytical method for determining the set of Haar orthogonal wavelets and a proposal for synthesizing the inverter's output waveforms based on the wavelet transform are described. The control signals of the component two-level inverters forming a multi-level voltage inverter were calculated. The *cooperation of such an inverter with a resistive-inductive load was simulated.*

Słowa kluczowe: falka Haara, transformata falkowa, falownik wielopoziomowy, falownik pięciofazowy, synteza przebiegów falkowych. **Keywords**: Haar wavelet, wavelet transform, multilevel converter, five-phase inverter, wavelet waveforms synthesis.

Introduction

Over the past few decades, inverters have become a popular technology. The great interest in optimizing and improving the efficiency of inverters is justified by the continuous demand for their use in various industry sectors, such as electric drives, power systems, and renewable energy systems. One of the goals of this research effort is to develop and experimentally test an electronic switching technique that allows the control of the inverter to produce input signals with reduced harmonic content, regardless of their ratings, configurations, types, and load conditions. Several inverter-switching techniques have been described in the literature and have been developed and tested to support both single-phase and three-phase inverters [1,2].

Devices such as voltage and current converters are now able to control and power a variety of devices operating in the power range of hundreds of kW and more. These devices have to fulfil definite and diversified requirements which implies diversified purposes and methods of electric energy conversion. There are many industrial applications e.g. uninterruptible power supplies (UPS) or distributed power generation systems, where the essential demand is to generate 50 or 60 Hz sinusoidal voltage waveforms. The quality of generated waveforms, especially the Total Harmonic Distortion factor (THD), should comply with appropriate standards [3, 4]. Similar requirements are to be fulfilled in converters applied in renewable energy systems.

Using modulation techniques, we can control the electronic switches to get the desired amplitude and frequency. Pulse width modulation (PWM) is a switching technique used with inverters. The most popular PWM modifications are sinusoidal pulse width modulation (SPWM), random pulse width modulation (RPWM), and space vector modulation (SVM). The main problem resulting from the use of PWM is the problem of higher harmonics, which can lead to damage to the inverter, noise, motor failure, increase in torque pulsation, or increase in motor temperature and shorten its life [2]. The features, performance, drawbacks, and limitations of the VSI inverter have been largely recognized and verified in practice [5].

Recently, multilevel inverters have emerged as a new and very important class of converters. They permit us to overcome the problem of limited power and shape output waveforms.

The development of multilevel converters comprises a novel area of research for new topologies, control strategies, and theory. Producing the required voltage or current waveforms is possible in many ways: e.g. sinusoidal PWM, selective harmonic elimination, space-vector modulation (SVM), or shaping the stepped voltage or current [6, 7]. Diverse methods of converters' control such as computing the adequate switching angles of stepped waveforms or cutting specified harmonics have been developed [7].

The paper deals with the technique of shaping the stepped output waveforms in multilevel converters. A mathematical approach to the control strategy based on wavelet transforms is presented. The output waveform synthesis is accomplished using a set of orthogonal wavelets. The discussion includes such mathematical tools as the Haar wavelet transform [7,8,9]. The article presents the initial simulation studies of the developed five-phase inverter model controlled by wavelet signals. This is the first model that enables the implementation of the wavelet theory presented in [7]. The next step will be an attempt to implement the presented inverter and experimental research.

Wavelet waveforms synthesis

Wavelets is a term for mathematical functions, that allow the analysis of signals in different time scales and resolutions. The wavelet applications are in many not directly related areas like seismology, video analysis, quantum mechanics, or electronics. The wavelets have been used mainly for analysing processes or signals based on the decomposition of the elements of the processes. The following considerations will prove that wavelets can also be useful in the composition of power electronics signals and structures. The main application of the wavelet theory is used in control algorithms [10,11,12] or diagnostics [13,14,15] and the detection [16] of various types of faults in converters, networks, or drives [2]. The Haar wavelet, on the other hand, is not so widely used.

The Haar wavelets have been adapted [6,7] for cascade inverter control. The Haar wavelet form is similar to the form of the voltage or current pulse that can be obtained using a simple one-phase inverter i.e. H-bridge cell. The displacement and width of the wavelet can be freely created and controlled. Thanks to these properties it is possible to apply wavelets in power electronics e.g. to form the output stepped waveforms of multilevel converters [16].

Let us define the scaling function *φ*(*x*) in an interval *x*<0,2π) :

(1)
$$
\varphi(x) = \begin{cases} 1 & \text{for } 0 \leq x < 2\pi, \\ 0 & \text{for other } x \end{cases}
$$

The fundamental proposed wavelet is defined:

(2)
$$
\psi(x) = \begin{cases} 1 & \text{for } 0 \leq x < \pi, \\ -1 & \text{for } \pi \leq x < 2\pi, \\ 0 & \text{for other } x \end{cases}
$$

The wavelet determines one period of the rectangular wave and is the mother function introducing a family of wavelets:

(3)
$$
\psi_{mn}(x) = \psi(2^{-m}x - 2\pi n)
$$
 for $m, n = ..., -2, -1, 0, 1, 2, ...$

The wavelet scale is done as 2*^m*2π and its displacement on the x-axis is determined as *n*-times 2*^m*+1π. The *m* factor scales not only the wavelet but the amplitude too.

The scaling function and a few wavelets have been presented in Figure 1.

Fig.1. The scaling function $\varphi(x)$ and wavelets $\psi_{mn}(x)$: a) scaling function $\varphi(x)$, b) fundamental wavelet $\psi_{00}(x)$, c) wavelets ψ -1-1(*x*), ψ -20(*x*), ψ -22(*x*), ψ -37(*x*)

All wavelets $\psi_{mn}(x)$ are orthogonal in the interval *x*<0,2π). The defined statement (7) creates a family of orthogonal functions and can determine the basis of the wavelet transform. A continuous wavelet transform is defined as:

(4)
$$
Wf(m,n) = \int_{-\infty}^{\infty} f(x) \psi_{mn}(x) dx
$$

and presents itself as a scalar product of functions *f*(*x*) and function $\psi_{mn}(x)$. The function $f(x)$ reconstruction occurs when the inverse wavelet transform is applied (5).

In applications more comfortable is using a discrete inverse wavelet transform which is defined according to (6) by the equation:

(5)
$$
f(x) = C \sum_{m=-\infty}^{\infty} \sum_{n=-\infty}^{\infty} Wf(m,n) \psi_{mn}(x)
$$

The symbol C denotes a constant that can be calculated from the Fourier transform of the function $w_{mn}(x)$. It could be written as a sum of wavelets $\psi_{mn}(x)$ multiplied by coefficients *amn*

(6)
$$
f(x) = \sum_{m=-\infty}^{\infty} \sum_{n=-\infty}^{\infty} a_{mn} \psi_{mn}(x)
$$

The coefficients *amn*, called wavelet coefficients, are scalar products of the function $f(x)$ and wavelets $\psi_{mn}(x)$. In the interval *x*<0,2π) they are given as

$$
(7) \t a_{mn} = C_m \int_0^{2\pi} f(x) \psi_{mn}(x) dx
$$

The constant C*m* is only dependent on coefficient *m* and has the same value for different *n*. Denoting

$$
(8) \t f_{mn}(x) = a_{mn} \psi_{mn}(x)
$$

it is possible to write $f_{\psi}(x)$ as a sum of components *f mn* (*x*):

(9)
$$
f_{\psi}(x) = \sum_{m=-3}^{m=0} \sum_{n=0}^{2^{-m}-1} f_{mn}(x)
$$

The components are *f mn* (*x*) present component wavelets, amplitude, and phase which are determined by coefficients *a mn*, calculated according to (7).

In power electronics, the most important criterion of the approximated waveforms is the THD factor. Practically in power electronics applications, the approximation of a sine wave should be realized using a finite number of wavelets. The natural aspiration of designers is to utilize the possibly lowest number of components. The accuracy of approximation depends on it. In mathematics, the accuracy is determined as an average square error δ , a very useful criterion destined for that purpose.

Let us denote by $f_\psi(\bm{x})$ a waveform approximating the function $f(x)=\sin(x)$ in the interval $x \in \leq 0, 2\pi$). Assuming that $f_{\psi}(x)$ forms a combination of wavelets determined by index $m = -3, -2, -1, 0$ it can be written as a sum:

(10)
$$
f_{\psi}(x) = \sum_{m=-3}^{m=0} \sum_{n=0}^{2^{-m}-1} a_{mn} \psi_{mn}(x) = \sum_{m=-3}^{m=0} \sum_{n=0}^{2^{-m}-1} f_{mn}(x)
$$

All coefficients *a mn*, calculated according to (7), have been collected in Table 1.

Fig. 2. The first step of wavelet approximation: $f_{\psi_1}(x)$, and the second step of wavelet approximation: $f_{\psi_2}(x)$

Table 1. The wavelet coefficients *a mn*.

	а mn						
	a _{<u>on</u>}	0.636					
	$a_{_{-\underline{1}n}}$						
	$a_{_{-2n}}$	-0.264	0.264		$0.264 - 0.264$		
	a_ ₃₀	-0.179	-0.074	0.074	0.179	0.179	0.074

Successive steps of wavelet approximation $f_{\psi_K}(x)$ for $k = 1, 2, 3$ have been presented in Figures 2, 3, and 4. The first step of reconstruction creates function $f_{\psi_1}(x)$ as a set of wavelets:

(11)
$$
f_{n}(x) = \sum_{m=-2}^{m=0} \sum_{n=0}^{2^{-m}-1} f_{mn}(x)
$$

and

(12)
$$
f_{\psi_1}(x) = f_{-20} + f_{-21} + f_{-22} + f_{-23} + f_{-10} + f_{-11} + f_{\psi_0}(x)
$$

in which two component wavelets are equal to zero according to Table 1. The waveforms are presented in Figure 2 (a). Figure 2 (b) presents the result of the second step of approximation in which a few (but not all) wavelets f _{3n} have been added to the function $f_{\psi_1}(x)$. Function $f_{\psi_2}(x)$ creates a composition of the following wavelets:

(13)
$$
f_{\psi_2}(x) = f_{-30} + f_{-33} + f_{-34} + f_{-37} + f_{\psi_1}(x)
$$

Wavelet converter

The topology of the single-phase cascade inverter was used to build a five-phase voltage inverter controlled by wavelet signals. For the first wavelet form, a five-phase voltage inverter consists of five three-level and single-phase voltage inverters (Fig. 3). There must be six inverters for three wavelet forms, and for three wavelet forms, there must be nine. In Figure 3, capital letters "A",...,"F" is assumed to denote components of the system, e.g. control blocks. load, etc. for a specific phase. The lowercase letters "a", "b", "c", "d" and "e" were used to describe the signals. In the further part of the study, examples of voltage and current waveforms mainly concern phase A.

Fig.3. Five-phase cascade inverter controlled by wavelet signals

In Figure 3, the blocks marked "inverter control" mean systems that form control signals for a given level of singlephase inverters. These blocks between the phases are the same - which makes the control system simple. The numbers entered next to the designation of system elements (e.g. the first number for transformers, the number in the "inverter control" block) means the next level of inverters in the created cascade. The block labeled "frequency" is the source of the triangular carrier signal. For each phase, these signals differ only in phase shift. The block marked "segments" is used by the logic of the "inverter control" block to determine the number of rectangular components forming a given wavelet signal. For the considered range of wavelets, only 16 segments are enough to reconstruct the shape of the function from Figures 1 and 2.

The article shows the shape of the output voltage using three wavelet forms that create the output voltage in each of the five phases. According to the authors, the output voltage reproduces the shape of the sinusoidal function with high accuracv.

The proposed method based on wavelet transform is very suitable for cascade converters even though it demands independent voltage sources. In each phase of the complex converter, the component inverters are supplied from three independent voltage sources: Udc1p, Udc2p, and Udc3p, where "p" is the index of individual phases: A, B, C, D, E. The supply voltages are proportional to the relevant amplitudes of component wavelets. If the proportional factor is set to 500 the voltage: Udc1p = 318 V, $Udc2p = 132 V$, $Udc3p = 50 V$.

The control of such a converter is derived from the described wavelets model. The inverter control signals, which were calculated from the Haar wavelet, are shown in Figure 4. The designation of the control signals in Figure 4 is consistent with the designation of the transistors in Figure 3. Figure 4 shows only those signals that differ, the other control signals for a given inverter are created by negation. For the remaining phases, the signals are similar except for the phase shift. These are control signals derived for the A phase and for three wavelet levels. The first inverter, counting from the top, generates the output voltage with the waveform $f_{\psi_{00}}$, the second with the waveform ψ_{2n} , and the third with the waveform ψ -3n.

The summing process of the wavelets in the cascade is accomplished by serially connecting the outputs of the component inverters whose switching frequencies differ.

Fig. 4. Signals controlling electronic switches - example for phase "a" and frequency 50 Hz

Simulation tests were carried out in the PLECS program and the RT Box device, which enables HIL (hardware in the loop) simulations. In simulation tests, the cooperation of the proposed inverter with resistive-inductive load was checked..

Figure 5 shows examples of voltages and current waveforms at the same load R_0 = 15 Ω and L₀ = 30 mH and for the fundamental frequency $f = 50$ Hz and $f = 20$ Hz.

In the research on the single-phase inverter described in [16], the shape of the output voltages was closer to the function calculated in Figures 1 and 2.

Selected parameters of the output voltages and currents of the cascade inverter for the considered wavelet signals used in the control are listed in Table 2. In particular, the RMS and THD values were focused on. As can be seen from the presented results, along with the complication of the control with successive wavelet functions, the THD content in the case of voltages and currents is significantly reduced.

Fig. 5. Voltage and current waveforms for 50 Hz (a) and their spectrum (40 harmonics) (b)

Table 2. Selected parameters of voltages and currents for the considered wave functions by the three frequencies (50 Hz, 20 Hz, 5 Hz)

Conclusion

The five-phase cascade inverter controlled by signals based on the Haar wavelet transform allows obtaining a waveform of the output voltage with a very low content of higher harmonics without using PWM modulation and additional filters. At the same time, an RL load with a low inductance value allows to obtain a current waveform similar to a sine wave with a low THD = 5.5 %. As a result, the proposed system with wavelet control is an interesting proposition in the case of intensively developing systems with multi-level inverters in renewable energy sources.
The presented simulation studies encourage

implementation of this control strategy in a real system. Verification tests in the real system are very effortless because in order to build a cascade inverter, according to the topology shown in Figure 3, only standard single-phase H-bridge inverters are needed as components.

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